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AN ORLIK–SOLOMON TYPE ALGEBRA FOR MATROIDS WITH A FIXED LINEAR CLASS OF CIRCUITS

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Abstract: A family C_L of circuits of a matroid M is a linear class if, given a modular pair of circuits in C_L , any circuit contained in the union of the pair is also in C_L . The pair (M, C_L) can be seen as a matroidal generalization of a biased graph. We introduce and study an Orlik–Solomon type algebra determined by (M, C_L) . If C_L is the set of all circuits of M this algebra is the Orlik–Solomon algebra of M.

1 – Introduction

Let $\mathcal{A}_{\mathbb{C}} = \{H_1, \ldots, H_n\}$ be a central and essential arrangement of hyperplanes in \mathbb{C}^d (i.e, such that $\bigcap_{H_i \in \mathcal{A}_{\mathbb{C}}} H_i = \{0\}$). The manifold $\mathfrak{M} = \mathbb{C}^d \setminus \bigcup_{H \in \mathcal{A}_{\mathbb{C}}} H$ plays an important role in the Aomoto–Gelfand multivariable theory of hypergeometric functions (see [9] for a recent introduction from the point of view of arrangement theory). There is a rank *d* matroid $M := M(\mathcal{A}_{\mathbb{C}})$ on the ground set [*n*] canonically determined by $\mathcal{A}_{\mathbb{C}}$: a subset $D \subseteq [n]$ is a dependent set of *M* if and only if there are scalars $\zeta_i \in \mathbb{C}$, $i \in D$, not all nulls, such that $\sum_{i \in D} \zeta_i \theta_{H_i} = 0$, where $\theta_{H_i} \in (\mathbb{C}^d)^*$ denotes a linear form such that $\operatorname{Ker}(\theta_{H_i}) = H_i$.

Let M be a matroid and M^* be its dual. In the following, we suppose that the ground set of M is $[n] := \{1, 2, ..., n\}$ and its rank function is denoted by r_M .

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The subscript M in r_M will often be omitted. Let $\mathcal{C} = \mathcal{C}(M)$ be the family of circuits of M. Let \mathbf{K} be a field and $E = \{e_1, \ldots, e_n\}$ be a finite set of order n. Let $\bigoplus_{e \in E} \mathbf{K} e$ be the vector space over \mathbf{K} of basis E and \mathcal{E} be the graded exterior algebra $\bigwedge (\bigoplus_{e \in E} \mathbf{K} e)$, i.e.,

$$\mathcal{E} := \sum_{i=0} \mathcal{E}_i = \mathcal{E}_0(=\mathbf{K}) \oplus \mathcal{E}_1\left(=\bigoplus_{e \in E} \mathbf{K}e\right) \oplus \cdots \oplus \mathcal{E}_i\left(=\bigwedge^i \left(\bigoplus_{e \in E} \mathbf{K}e\right)\right) \oplus \cdots$$

For every linearly ordered subset $X = \{i_1, \ldots, i_m\} \subseteq [n], i_1 < \cdots < i_m$, let e_X be the monomial $e_X := e_{i_1} \land e_{i_2} \land \cdots \land e_{i_m}$. By definition set $e_{\emptyset} = 1 \in \mathbf{K}$. Consider the map $\partial: \mathcal{E} \to \mathcal{E}$, extended by linearity from the "differentials", $\partial e_i = 1$ for every $e_i \in E, \ \partial e_{\emptyset} = 0$ and

$$\partial e_X = \partial (e_{i_1} \wedge \cdots \wedge e_{i_m}) = \sum (-1)^j e_{i_1} \wedge \cdots \wedge e_{i_{j-1}} \wedge e_{i_{j+1}} \wedge \cdots \wedge e_{i_m}.$$

The (graded) Orlik–Solomon K-algebra OS(M) of the matroid M is the quotient \mathcal{E}/\Im where \Im denotes the (homogeneous) two-sided ideal of \mathcal{E} generated by the set

$$\left\{\partial e_C \colon C \in \mathcal{C}(M), \ |C| > 1\right\} \cup \left\{e_C \colon C \in \mathcal{C}(M), \ |C| = 1\right\}$$

or equivalently by the set

$$\left\{\partial e_C \colon C \in \mathcal{C}(M), \ |C| > 1\right\} \cup \left\{e_C \colon C \in \mathcal{C}(M)\right\}$$

The de Rham cohomology algebra $H^{\bullet}(\mathfrak{M}(\mathcal{A}_{\mathbb{C}}); \mathbf{K})$ is shown to be isomorphic to the Orlik–Solomon **K**-algebra of the matroid $M(\mathcal{A}_{\mathbb{C}})$, see [6, 7]. We refer to [5] for a recent discussion on the role of matroid theory in the study of Orlik–Solomon algebras.

2 - Linear class of circuits

Given a family \mathcal{C} of circuits of a matroid M set

$$\mathcal{H}(\mathcal{C}) := \left\{ H(C) = [n] \backslash C \colon C \in \mathcal{C}_L \right\}$$

be the associated family of hyperplanes of M^* . We recall that a pair $\{X, Y\}$ of subsets of the ground set [n] is a modular pair of M([n]) if

$$\mathbf{r}(X) + \mathbf{r}(Y) = \mathbf{r}(X \cup Y) + \mathbf{r}(X \cap Y) .$$

Proposition 2.1. Let $\{C_1, C_2\}$ be a pair of circuits of M and $\{H(C_1), H(C_2)\}$ be the associated hyperplanes of M^* . The following four conditions are equivalent:

- $\circ \{C_1, C_2\}$ is a modular pair of circuits of M,
- $\{H(C_1), H(C_2)\}$ is a modular pair of hyperplanes of M^* ,
- $r_M(C_1 \cup C_2) = |C_1 \cup C_2| 2,$
- $r_{M^{\star}}(H(C_1) \cap H(C_2)) = r(M^{\star}) 2 (= n r 2).$

Definition 2.2 ([10]). We say that the family of circuits C', $C' \subseteq C(M)$, is a *linear class of circuits* if, given a modular pair of circuits in C', all the circuits contained in the union of the modular pair are also in C'. \Box

In the following we will always denote by C_L a linear class of circuits of the matroid M.

Definition 2.3. We say that the family \mathcal{H} of hyperplanes of M is a *linear* class of hyperplanes of M if, given a modular pair of hyperplanes in \mathcal{H} , all the hyperplanes of M containing the intersection of the pair are also in \mathcal{H} . \Box

The following corollary is a direct consequence of Proposition 2.1 and Definitions 2.2 and 2.3.

Corollary 2.4. The following two assertions are equivalent:

- The family \mathcal{C}' is a linear class of circuits of M;
- The set $\mathcal{H}(\mathcal{C}')$ is a linear class of hyperplanes of M^* .

Remark 2.5. The linear class of hyperplanes $\mathcal{H}(\mathcal{C}_L)$ of M^* determines a single-element extension

$$M^{\star}([n]) \stackrel{\mathcal{H}(\mathcal{C}_L)}{\hookrightarrow} N^{\star}([n+1]),$$

where $\{n+1\}$ is in the closure in $N^*([n+1])$ of a hyperplane H of $M^*([n])$, if and only if $H \in \mathcal{H}(\mathcal{C}_L)$. Two special cases occur:

- If $C_L = C(M)$ the element n + 1 is a coloop of N([n + 1]).
- If $C_L = \emptyset = \mathcal{H}(C_L)$ the element n+1 is a is in general position in $N^*([n+1])$.

In the literature N([n+1]) is called the extended lift of M([n]) (determined by the linear class of circuits C_L). \Box

Lemma 2.6. Let N = N([n+1]) be the extended lift of M([n]) determined by the linear class of circuits C_L , $C_L \neq \emptyset$, C(M). Then N has the family of circuits:

$$\mathcal{C}(N) = \begin{cases} \mathcal{C}_L \cup \mathcal{C}_1 & \text{if } \left| \bigcup_{C \in \mathcal{C}_L} C \right| - \mathbf{r}_M \left(\bigcup_{C \in \mathcal{C}_L} C \right) = n - r - 1 ; \\ \mathcal{C}_L \cup \mathcal{C}_1 \cup \mathcal{C}_2 & \text{otherwise} , \end{cases}$$

where

$$\mathcal{C}_1 := \left\{ C \cup \{n+1\} \colon C \in \mathcal{C}(M) \backslash \mathcal{C}_L \right\},\$$
$$\mathcal{C}_2 := \left\{ C' \cup C'' \colon C', C'' \text{ is a modular pair of } \mathcal{C}(M) \backslash \mathcal{C}_L \right\}.$$

Proof: The matroid $N^{\star}([n+1])$ has the family of hyperplanes:

$$\mathcal{H}(N^*) = \begin{cases} \mathcal{H}_0 \cup \mathcal{H}_1 & \text{if } \mathbf{r}_{M^*} \left(\bigcap_{C \in \mathcal{C}_L} H(C) \right) = 1 ; \\ \mathcal{H}_0 \cup \mathcal{H}_1 \cup \mathcal{H}_2 & \text{otherwise} , \end{cases}$$

where

$$\begin{aligned} \mathcal{H}_0 &:= \left\{ H \cup \{n+1\} \colon H \in \mathcal{H}(\mathcal{C}_L) \right\} ,\\ \mathcal{H}_1 &:= \left\{ H(C') \colon C' \in \mathcal{C}(M) \backslash \mathcal{C}_L \right\} ,\\ \mathcal{H}_2 &:= \left\{ H' \cap H'' \cup \{n+1\} \colon H', H'' \text{ is a modular pair of } \mathcal{H}(\mathcal{C}(M) \backslash \mathcal{C}_L) \right\} . \end{aligned}$$

3 - A bias algebra

The pair (M, \mathcal{C}_L) can be seen as a matroidal generalization of the pair (G, \mathcal{C}_L) (defining a biased graph) where G is a graph and \mathcal{C}_L a set of balanced circuits of G. A biased graph is a graph together with a (linear) class of circuits which are called balanced. It is a generalisation of signed and gain graphs which are related to some special class of hyperplane arrangements. In the classical graphic hyperplane arrangements, a hyperplane has equation of the form $x_i = x_j$. In the "signed graphic" arrangements, the equations can be of the form $x_i = \pm x_j$. In the "gain graphic" arrangements, the equations can be of the form $x_i = gx_j$ (in the biased case) or of the form $x_i = x_j + g$ (in the lift case). All these definitions due to T. Zaslavsky are very natural and produce a nice theory [12, 13] in connection with graphs, matroids and arrangements. The following bias algebra is close related to the biased graphs (and its matroidal generalizations).

AN ORLIK–SOLOMON TYPE ALGEBRA FOR MATROIDS

Definition 3.1. Let C_L be a linear class of circuits of the matroid M([n])and N = N([n+1]) be the extended lift of M([n]) determined by C_L . Let OS(N)be the Orlik–Solomon K-algebra of the matroid N. The bias K-algebra of the pair (M, C_L) , denoted $Z(M, C_L)$, is the graded quotient of the Orlik–Solomon algebra OS(N) by the two-sided ideal generated by e_{n+1} , i.e.,

$$\operatorname{Z}(M,\mathcal{C}_L) := \operatorname{OS}(N)/\langle e_{n+1} \rangle$$
 . \square

Remark 3.2 ([11]). This algebra is also known as the Orlik–Solomon algebra of the pointed matroid N, with basepoint n + 1, see [5, Definition 3.2]. If N may be realized by a complex hyperplane arrangement, then $Z(M, C_L)$ is isomorphic to the cohomology ring of the complement of the decone of this arrangement with respect to the $(n+1)^{\text{st}}$ hyperplane, [7, Corollary 3.57]. Two special cases occur when M itself is realizable and C_L is either all of $\mathcal{C}(M)$ or the empty set. Indeed, suppose that M is the matroid associated to a complex hyperplane arrangement \mathcal{A} . Then $Z(M, \mathcal{C}(M))$ is isomorphic to the cohomology of the complement of \mathcal{M} (i.e., the Orlik–Solomon algebra of M), and $Z(M, \emptyset)$ is isomorphic to the cohomology of the complement of the arrangement attained by translating each of the hyperplanes of \mathcal{A} some distance away from the origin, so that every dependent set will have empty intersection. \Box

Theorem 3.3. The bias **K**-algebra $Z(M, C_L)$ is independent of the order of the elements of M([n]), i.e., it is an invariant of the pair (M, C_L) . For every linear class C_L , the algebra $Z(M, C_L)$ is isomorphic to the quotient of the exterior **K**-algebra

(3.1)
$$\mathcal{E} := \bigwedge \left(\bigoplus_{i=1}^{n} \mathbf{K} e_{i} \right)$$

by the two-sided ideal $\langle \Im(\mathcal{C}_L) \rangle$ generated by the set

$$\Im(\mathcal{C}_L) := \left\{ \partial e_C \colon C \in \mathcal{C}_L, \ |C| > 1 \right\} \cup \left\{ e_C \colon C \in \mathcal{C}(M) \right\}.$$

Proof: Since the Orlik–Solomon **K**-algebra OS(N) does not depend of the ordering of the ground set the first part of the theorem follows. The second assertion is a straightforward consequence of Lemma 2.6.

As the element e_{n+1} does not appear in the algebra $Z(M, \mathcal{C}_L)$ we will omit it. We remark that the monomial e_X , $X \subseteq [n]$, in $Z(M, \mathcal{C}_L)$ is different from zero if and only if X is an independent set of M.

Corollary 3.4. The bias **K**-algebra Z(M, C(M)) is the Orlik–Solomon **K**-algebra of OS(M). Furthermore the bias **K**-algebra $Z(M, \emptyset)$ is isomorphic to the quotient of the exterior algebra (3.1) by the two-sided ideal generated by the set $\{e_C : C \in C(M)\}$.

Definition 3.5. Given an independent set I, a non-loop element $x \in cl(I) \setminus I$ is said to be \mathcal{C}_L -active in I if C(x, I) (i.e., the unique circuit contained in $I \cup x$) is a circuit of the family \mathcal{C}_L and x is the smallest element of C(x, I). An independent set with at least one \mathcal{C}_L -active element is said to be \mathcal{C}_L -active, and \mathcal{C}_L -inactive otherwise. We denote by a(I) the smallest \mathcal{C}_L -active element in an active independent set I. \Box

Definition 3.6. We say that a subset $U \subseteq [n]$ is a \mathcal{C}_L -unidependent (set of M) if it contains a unique circuit C(U) of M, $C(U) \in \mathcal{C}_L$ and |C(U)| > 1.

We say that a \mathcal{C}_L -unidependent set U is \mathcal{C}_L -inactive if the minimal element of C(U), min C(U), is the the smallest \mathcal{C}_L -active element of the independent set $U \setminus \min C(U)$. Otherwise the set U is said \mathcal{C}_L -active. \Box

Definition 3.7. For every circuit $C \in C_L$, |C| > 1, the set $C \setminus \min(C)$, is said to be a C_L -broken circuit. The family of C_L -inactive independents, denoted NBC_{C_L} , is the family of independent sets of M not containing a C_L -broken circuit. \Box

Set

$$\mathbf{nbc}_{\mathcal{C}_L} := \left\{ e_I \colon I \in \mathrm{NBC}_{\mathcal{C}_L} \right\},$$
$$\mathbf{b}_{\Im(\mathcal{C}_L)} := \left\{ \partial e_U \colon U \text{ is } \mathcal{C}_L \text{-inactive unidependent} \right\} \cup \left\{ e_D \colon D \text{ is dependent} \right\}.$$

Theorem 3.8. The sets $\mathbf{nbc}_{\mathcal{C}_L}$ and $\mathbf{b}_{\mathfrak{S}(\mathcal{C}_L)}$ are bases, respectively of the bias **K**-algebra $Z(M, \mathcal{C}_L)$ and of the ideal $\langle \mathfrak{S}(\mathcal{C}_L) \rangle$.

Proof: We will show the two statements at the same time by proving that both sets are spanning and that they have the correct size. Let I be an independent set of M. If I is C_L -active then we have

$$e_I = \sum_{x \in C(a(I),I) \setminus a(I)} \zeta_x e_{I \cup a(I) \setminus x}$$

where $\zeta(x) \in \{-1, 1\}$. This is an expression for e_I whit respect to lexicographically smaller e_X where X is an independent of M and |X| = |I|. By induction, we get that the set $\mathbf{nbc}_{\mathcal{C}_L}$ is a generator of the graded algebra $Z(M, \mathcal{C}_L)$.

AN ORLIK–SOLOMON TYPE ALGEBRA FOR MATROIDS

369

Let U be a \mathcal{C}_L -unidependent set of M. Suppose that U is \mathcal{C}_L -active and let $a = \min C(U)$ and set $I := C(U) \setminus a$. Note that $\{C(U), C(a(I), I)\}$ is a modular pair of circuits of \mathcal{C}_L , so every circuit contained in the cycle $C(U) \cup C(a(I), I)$ is in \mathcal{C}_L . From the definition of the map ∂ we know that

$$\partial e_U = \sum_{x \in C(U) \setminus a} \epsilon_x \, \partial e_{U \cup a(I) \setminus x} \; ,$$

where $\epsilon_x \in \{-1, 1\}$. This is an expression for ∂e_U with respect to lexicographically smaller ∂e_X , where X is a \mathcal{C}_L -unidependent and |U| = |X|. By induction, we get that the set $\mathbf{b}_{\mathfrak{S}(\mathcal{C}_L)}$ is a generator of $\langle \mathfrak{S}(\mathcal{C}_L) \rangle$. By the definition of $Z(M, \mathcal{C}_L)$, we know that

$$\dim \left(\mathbb{Z}(M, \mathcal{C}_L) \right) + \dim \left(\langle \Im(\mathcal{C}_L) \rangle \right) = \dim(\mathcal{E}) = 2^n$$

Given a subset X of [n], it is either dependent or independent C_L -active or independent C_L -inactive. To every independent C_L -active independent set I corresponds uniquely the unidependent C_L -inactive $I \cup a(I)$. We have then that

$$\left|\mathbf{nbc}_{\mathcal{C}_{L}}(M)\right| + \left|\mathbf{b}_{\mathfrak{F}(\mathcal{C}_{L})}\right| = 2^{n}$$
.

We define the deletion and contraction operation for an arbitrary subset of circuits $\mathcal{C}' \subseteq \mathcal{C}(M)$ setting:

$$\mathcal{C}' \backslash x := \left\{ C \in \mathcal{C}' \colon x \notin C \right\}$$

and

$$\mathcal{C}'\!/x := \begin{cases} \mathcal{C}' \backslash x & \text{if } x \text{ is a loop of } M \,, \\ \left\{ C \backslash x \colon x \in C \in \mathcal{C}' \right\} \uplus \left\{ C \in \mathcal{C}' \colon x \notin \mathrm{cl}_M(C) \right\} & \text{otherwise }. \end{cases}$$

From the preceding definition, we can see that given a circuit C of \mathcal{C}'/x , where x is a non-loop of M, there exists a unique circuit $\widehat{C} \in \mathcal{C}'$ such that

$$\widehat{C} := \begin{cases} C \cup x & \text{if } x \in \operatorname{cl}_M(C) \\ C & \text{otherwise }. \end{cases}$$

Proposition 3.9. Let M be a matroid and C_L be a linear class of circuits of M. For an element x of the matroid, the circuit sets $C_L \setminus x$ and C_L / x are linear classes of $M \setminus x$ and M / x, respectively.

Proof: The statement for the deletion is clear. If x is a loop the result is also clear for the contraction. Suppose that x is a non-loop of M. If $Y \subseteq X$ are sets such that $r_M(X) = r_M(Y) + 1$ then we have

(3.2)
$$\mathbf{r}_{M/x}(X \setminus x) = \mathbf{r}_{M/x}(Y \setminus x) + \epsilon , \quad \epsilon \in \{0, 1\} .$$

So, if $\{C_1, C_2\}$ is a modular pair of circuits of \mathcal{C}_L/x , $\{\widehat{C}_1, \widehat{C}_2\}$ is also a modular pair of circuits of \mathcal{C}_L . We see also from Equation 3.2 that if $C \subseteq C_1 \cup C_2$ is a circuit of M/x then $\widehat{C} \subseteq \widehat{C}_1 \cup \widehat{C}_2$, so $\widehat{C} \in \mathcal{C}_L$ and necessarily $C \in \mathcal{C}_L/x$.

Definition 3.10. For a pair (M, \mathcal{C}_L) and an element x of M, we define the deletion and the contraction of the pair (M, \mathcal{C}_L) by:

and

$$(M, \mathcal{C}_L) \setminus x := (M \setminus x, \mathcal{C}_L \setminus x)$$

 $(M, \mathcal{C}_L) / x := (M / x, \mathcal{C}_L / x) . \square$

As a corollary of Theorem 3.3 we have:

Proposition 3.11. For every element x of M, there is a unique monomorphism of vector spaces,

$$\mathfrak{i}_x\colon \operatorname{Z}(M,\mathcal{C}_L)\backslash x\to \operatorname{Z}(M,\mathcal{C}_L)$$
,

such that, for every independent set I of $M \setminus x$, we have $i_x(e_I) = e_I$.

Proposition 3.12. For every non-loop element x of M, there is a unique epimorphism of vector spaces, $\mathfrak{p}_x \colon \mathbb{Z}(M, \mathcal{C}_L) \to \mathbb{Z}(M, \mathcal{C}_L)/x$, such that, for every subset $I = \{i_1, \ldots, i_\ell\} \subseteq [n]$,

(3.3)
$$\mathbf{p}_{x}e_{I} := \begin{cases} e_{I\setminus x} & \text{if } x \in I ,\\ \pm e_{I\setminus y} & \text{if } \exists y \in I \text{ such that } \{x, y\} \in \mathcal{C}_{L} ,\\ 0 & \text{otherwise }. \end{cases}$$

More precisely the value of the coefficient ± 1 in the second case is the sign of the permutation obtained by replacing y by x in I.

Proof: From Theorem 3.3, it is enough to prove that the map \mathfrak{p}_x is well determined, i.e., for all \mathcal{C}_L -unidependent $U = (i_1, \ldots, i_m)$ set of M, we have

$$\mathfrak{p}_x \partial e_U = 0 \in \mathfrak{S}(\mathcal{C}_L/x)$$
.

AN ORLIK–SOLOMON TYPE ALGEBRA FOR MATROIDS

We can also suppose that x is the last element n. Note that if $n \in U$ then $U \setminus n$ is a \mathcal{C}_L/n -unidependent set of M/n. If $n \notin U$ but there is $y \in U$ and $\{n, y\} \in \mathcal{C}_L$, we know that $e_U = \pm e_{U \setminus y \cup n}$ in $Z(M, \mathcal{C}_L)$. Suppose that $n \notin U$ and that there does not exist $y \in U$ such that $\{n, y\} \in \mathcal{C}_L$. Then it is clear that $\mathfrak{p}_n \partial e_U = 0$. Suppose that $n \in U$. It is easy to see that

$$\pm \mathfrak{p}_n \,\partial e_U = \sum_{j=1}^{m-1} e_{U \setminus \{j,n\}} = 0$$

Finally, if an independent set I of M contains an element y such that $\{x, y\}$ is a circuit in \mathcal{C}_L , we know that there is a scalar $\chi(I; x, y) \in \{-1, 1\}$ such that $e_I = \chi(I; x, y) e_{I \setminus y \cup x}$. More precisely the value of $\chi(I; x, y) \in \{-1, 1\}$ is the sign of the permutation obtained by replacing y by x in I.

Theorem 3.13. Let M be a loop free matroid and C_L be a linear class of circuits of M. For every element x of M, there is a splitting short exact sequence of vector spaces

$$(3.4) \qquad 0 \ \to \ \mathbf{Z}(M,\mathcal{C}_L) \backslash x \ \stackrel{\mathfrak{i}_x}{\longrightarrow} \ \mathbf{Z}(M,\mathcal{C}_L) \ \stackrel{\mathfrak{p}_x}{\longrightarrow} \ \mathbf{Z}(M,\mathcal{C}_L)/x \ \to \ 0 \ .$$

Proof: From the definitions we know that $\mathfrak{p}_x \circ \mathfrak{i}_x$, is the null map so $\operatorname{Im}(\mathfrak{i}_x) \subseteq \operatorname{Ker}(\mathfrak{p}_x)$. We will prove the equality $\dim(\operatorname{Ker}(\mathfrak{p}_n)) = \dim(\operatorname{Im}(\mathfrak{i}_n))$. By a reordering of the elements of [n] we can suppose that x = n. The minimal \mathcal{C}_L/n -broken circuits of M are the minimal sets X such that either X or $X \cup \{n\}$ is a \mathcal{C}_L -broken circuit of M (see [1, Proposition 3.2.e]). Then

$$\operatorname{NBC}_{\mathcal{C}_L/n} = \left\{ X \colon X \subseteq [n-1] \text{ and } X \cup \{n\} \in \operatorname{NBC}_{\mathcal{C}_L} \right\}$$

and we have

(3.5)
$$\operatorname{NBC}_{\mathcal{C}_L} = \operatorname{NBC}_{\mathcal{C}_L \setminus n} \uplus \left\{ I \cup n \colon I \in \operatorname{NBC}_{\mathcal{C}_L / n} \right\}.$$

So $\dim(\operatorname{Ker}(\mathfrak{p}_n)) = \dim(\operatorname{Im}(\mathfrak{i}_n))$. There is a morphism of vector spaces

$$\mathfrak{p}_n^{-1}$$
: $\mathcal{Z}(M, \mathcal{C}_L)/n \to \mathcal{Z}(M, \mathcal{C}_L)$,

where, for every $I \in \text{NBC}_{\mathcal{C}_L/n}$, we have $\mathfrak{p}_n^{-1}e_I := e_{I\cup n}$. It is clear that $\mathfrak{p}_n \circ \mathfrak{p}_n^{-1}$ is the identity map. From Equation (3.5) we conclude that the exact sequence (3.4) splits. \blacksquare

372

Remark 3.14. A large class of algebras, the so called χ -algebras (see [4] for more details), contain the Orlik–Solomon, Orlik–Terao [8] (associated to vectorial matroids) and Cordovil algebras [3] (associated to oriented matroids). Following the same ideas it is possible to generalize the definition of the bias algebras and obtain a class of bias χ -algebras, determined by a pair (M, \mathcal{C}_L) , and that contain all the mentioned algebras. \Box

Similarly to [4], we now construct, making use of iterated contractions, the dual basis $\mathbf{nbc}^*_{\mathcal{C}_L}$ of the standard basis $\mathbf{nbc}_{\mathcal{C}_L}$. Let $Z(M, \mathcal{C}_L)_h$ be the subspace of $Z(M, \mathcal{C}_L)$ generated by the set

$$\left\{ e_X \colon X \text{ is an independent set of } M \text{ and } |X| = h \right\}$$

We associate to the (linearly ordered) independent set $I = (i_1, \ldots, i_h)$ of Mthe linear form on $Z(M, C_L)_h$, $\mathbf{p}_I : Z(M, C_L)_h \to \mathbf{K}$,

$$(3.6) \qquad \qquad \mathbf{\mathfrak{p}}_I := \mathbf{\mathfrak{p}}_{e_{i_1}} \circ \mathbf{\mathfrak{p}}_{e_{i_2}} \circ \cdots \circ \mathbf{\mathfrak{p}}_{e_{i_k}}.$$

We also associate to the linearly ordered independent $I = (i_1, \ldots, i_j)$ the flag of its final independent subsets, defined by

$$\left\{I_t: I_t = (i_t, \dots, i_j), \ 1 \le t \le j\right\}.$$

Proposition 3.15. Let $I = (i_1, \ldots, i_h)$ and $J = (j_1, \ldots, j_h)$ be two linearly ordered independents of M, then we have $\mathfrak{p}_I(e_J) \neq 0$ if and only if there is a permutation $\tau \in \mathfrak{S}_h$ such that for every $1 \leq t \leq h$, $j_{\tau(t)} \in \operatorname{cl}(I_t)$ and $C(j_{\tau(t)}, I_t) \in \mathcal{C}_L$. When the permutation τ exists, it is unique and we have $\mathfrak{p}_I(e_J) = \operatorname{sgn}(\tau)$. In particular we have $\mathfrak{p}_I(e_I) = 1$ for any independent set I.

Proof: The first equivalence is very easy to prove in both directions. To obtain the expression of $\mathbf{p}_I(e_J)$ we just need to iterate h times the formula of contraction of Proposition 3.11. With the definition of the permutation τ we know that $\mathbf{p}_I(e_{\tau(1)} \wedge \cdots \wedge e_{\tau(h)}) = 1$. By the antisymmetric of the wedge product we also have that $e_J = \operatorname{sgn}(\tau) \times e_{\tau(1)} \wedge \cdots \wedge e_{\tau(h)}$. And finally the last result comes from the fact that if I = J then clearly $\tau = \operatorname{id}$.

Theorem 3.16. The set $\{\mathbf{p}_I : I \in NBC_{\mathcal{C}_L}\}$ is the dual basis of the standard basis $\mathbf{nbc}_{\mathcal{C}_L}$ of $Z(M, \mathcal{C}_L)$.

Proof: Pick two elements e_I and e_J in $\mathbf{nbc}_{\mathcal{C}_L}$, |I| = |J| = h. We just need to prove that $\mathfrak{p}_I(e_J) = \delta_{IJ}$ (the Kronecker delta). From the preceding proposition we already have that $\mathfrak{p}_I(e_I) = 1$. Suppose for a contradiction that there exists a permutation τ such that $j_{\tau(t)} \in \operatorname{cl}(I_t)$ and $C(j_{\tau(t)}, I_t) \in \mathcal{C}_L$ for every $1 \leq t \leq h$. Suppose that $j_{\tau(m+1)} = i_{m+1}, \ldots, j_{\tau(h)} = i_h$ and $i_m \neq j_{\tau(m)}$. Then there is a circuit $C \in \mathcal{C}_L$ such that

$$i_m, j_{\tau(m)} \in C \subseteq \{i_m, j_{\tau(m)}, i_{m+1}, i_{m+2}, \dots, i_h\}$$

If $j_{\tau(m)} < i_m$ [resp. $i_m < j_{\tau(m)}$] we conclude that $I \notin \text{NBC}_{\mathcal{C}_L}$ [resp. $J \notin \text{NBC}_{\mathcal{C}_L}$], a contradiction.

The following corollary is an extension of results of [2], [3] and [4].

Corollary 3.17. Let $J = \{j_1, \ldots, j_\ell\}$ be an independent set of M such that the expansion of e_J in $\mathbf{nbc}_{\mathcal{C}_L}$ is $e_J = \sum_{I \in \mathbf{nbc}_{\mathcal{C}_L}} \xi(I, J) e_I$. Then the following are equivalent:

- $\circ \ \xi(I,J) \neq 0 \,,$
- there exists a permutation τ such that $e_{\tau(t)} \in \operatorname{cl}(I_t)$ and $C(j_{\tau(t)}, I_t) \in \mathcal{C}_L$ for every $1 \leq t \leq h$. Moreover, in the case where $\xi(I, J) \neq 0$ we have $\xi(I, J) = \operatorname{sgn}(\tau)$.

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