## Preprints

1. The solutions to Elmsley's problem, (with P. Diaconis), to appear in Math Horizons.
2. Quasi-random graphs with given degree sequences, (with F.R.K. Chung).
3. Minimum perimeter rectangles that enclose congruent non-overlapping circles, (with B.D. Lubachevsky).
4. Maximizing Data Locality in Distributed Systems, (with F.R.K. Chung, R. Bhagwan, S. Savage, and G.M. Voelker), to appear in Jour. Comp. System Sci..
5. Products of universal cycles, (with P. Diaconis).
6. Universal juggling cycles, (with F.R.K. Chung), to appear in Integers.
7. Primitive juggling sequences, (with F.R.K. Chung), to appear in American Mathematical Monthly.
8. Some of my favorite problems in Ramsey Theory, to appear in Integers.
9. Optimal tree structure for group key management with batch updates, (with M. Li and F.F. Yao), to appear in SIAM J. on Disc. Math.
10. On minimal colorings without monochromatic solutions to a linear equation, (with B. Alexeev and J. Fox), to appear in Integers.
11. Parallelism versus Memory Allocation in Pipelined Router Forwarding Engines, (with F.R.K. Chung, J. Mao and G. Varghese), to appear in ACM Transactions on Computer Systems.
12. Communication Latency Aware Low Power NoC Synthesis, (with Y. Hu, Y. Zhu, H. Chen and C.-K. Cheng).
13. Enumerating (multiplex) juggling sequences, (with S. Butler).
14. Constructing zero-deficiency parallel prefix circuits of minimum depth, (with H. Zhu, and C.-K. Cheng), to appear in ACM Transactions on Design Automation of Electronic Systems 21 pp.
15. On the growth of a van der Waerden-like function.
16. How to play the majority game with liars, (with S. Butler and J. Mao).
17. Timing model reduction for hierarchical timing analysis, (with S. Zhou, Y. Zhu, Y. Hu, M. Hutton, and C.-K. (heng), to appear.

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Apollonian circle packings: geometry and group theory II. Super-Apollonian group and integral packings, (with J.C. Lagarias, C.L. Mallows, A.R. Wilks, and C.H. Yan), Discrete \& Computational Geometry 35 (2006), no. 1, 1-36.
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3. Monochromatic equilateral right triangles on the integer grid, (with J. Solymosi), Topics in Discrete Mathematics, Algorithms and Combinatorics 26, 129-132.

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4. A Hierarchical Three-Way Interconnect Architecture for Hexagonal Processors, (with F. Zhou, E.Y. Cheng, B. Yao, and C.-K. Cheng), Int. Workshop on System Level Interconnect Prediction, pp. 133-139, April 2003.
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